



| | | VDDout | | Connecteur I/O | | | |
|-------------------|-----|----------|----|----------------|----------|-----|---------------------|
| right_ext_sensor | RA2 | ConAN2 | 1 | 2 | ConAN1 | RA1 | right_int_sensor |
| stop_switch (fdc) | RB2 | ConB2 | 3 | 4 | ConAN3 | RA3 | left_int_sensor |
| reserved i2c | RB1 | ConB1 | 5 | 6 | ConT0CLK | RA4 | right_encoder |
| reserved i2c | RB0 | ConB0 | 7 | 8 | ConAN4 | RA5 | left_ext_sensor |
| left_sign | RE1 | ConAN6 | 9 | 10 | ConAN5 | RE0 | right_sign |
| left_encoder | RC0 | ConT1CLK | 11 | 12 | ConAN7 | RE2 | start_switch (jack) |
| left_pwm | RC2 | ConPWM1 | 13 | 14 | ConPWM2 | RC1 | right_pwm |
| | | | 15 | 16 | | | |

Header 8X2